

2825

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Umesh Nair
Assignee: Sun Microsystems, Inc.
Title: System for Optimizing Buffers in Integrated Circuit Design Timing Fixes
Serial No.: 10/600,740 Filed: June 20, 2003
Examiner: Unknown Group Art Unit: Unknown
Docket No.: P8987 Customer No. 33438

Austin, Texas
September 11, 2003

COMMISSIONER FOR PATENTS
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Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

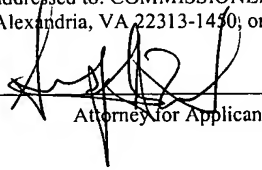
Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, Applicants wish to call the following documents to the attention of the Examiner.


A PTO form 1449 listing these documents is enclosed.

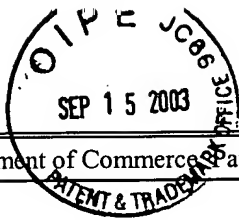
Citation of the above documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described above; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: COMMISSIONER FOR PATENTS, PO Box 1450, Alexandria, VA 22313-1450, on September 11, 2003.	
 Attorney for Applicant(s)	9/11/03 Date of Signature

Respectfully submitted,


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Reg. No. 32,946



U.S. Department of Commerce Patent and Trademark Office					Attorney Docket No.		Serial No.	
					P8987		10/600,740	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Applicant(s)			
(Use several sheets if necessary)					Umesh Nair			
					Filing Date		Group	
					June 20, 2003		Unknown	
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA	5,623,608	Apr. 22, 1997	Ng	395	250		
	AB	5,958,027	Sep. 28, 1999	Gulick	710	52		
	AC	6,392,747	May 21, 2002	Allen et al.	356	141.1		
	AD	6,550,045 B1	Apr. 15, 2003	Lu et al.	716	6		
	AE							
	AF							
	AG							
	AH							
	AI							
	AJ							
	AK							
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AL						<input type="checkbox"/>	<input type="checkbox"/>
	A						<input type="checkbox"/>	<input type="checkbox"/>
	AN						<input type="checkbox"/>	<input type="checkbox"/>
	AO							
	AP							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	AR							
	AS							
	AT							
Examiner			Date Considered					
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>								